## Exercise MCQs

Sr. No.	Questions	A	В	<b>C</b>	D
1	The process by which electrons are emitted by a hot metal surface is known as	Boiling	evaporation	conduction	√thermionic emission
2	The particles emitted from a hot cathode surface are	positive ions	negative ions	protons	✓Electrons
3	The logical operation performed by this gate is	AND	NOR	√NAND	0,0
4	AND gate can be formed by using two	NOT gates	OR gates	NOR gates	NAND gate
5	The output of a two-input NOR gate is 1 when:	A is '1' and B is '0'	A is '0' and B is '1'	✓ both A and B are 0	both A and I are '1'
6	If X = A.B, then X is '1' when:	✓ A and B are '1'	A or B is '0'	A is 0° and B is '1'	A is '1' and is '0'
7	The output of a NAND gate is '0' when	both of its inputs are '0'	✓both of its inputs are	any of its inputs is '0'	any of its inputs is '1'
		40			
		N/ 60			
	8e	Inputs are 0			
	acade!	my Fo			
	ce Acade	my Fo			
	ence Acade	my Ko			
	science Acade	my Ko			
	science Acade	my ko			
	science Acade	RNY KO			

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## **Additional MCQs**

1	Questions		D	C	D
	The biggest achievement of electronics is	Calculator	√Transistor	Computer	Mobile
2	The screen of cathode ray tube consist of material	Zinc	Iron	✓ Phosphor	Glass
3	The screen of cathode ray oscilloscope is	Reflecting	Refracting	Diffracting	√Fluorescer
4	CRO is used for	Measurement of voltage	To find range	To show wave form	✓ All of thes
5	In tungsten filament, the potential given to produce the beam of electrons by thermionic emission	√6 V	7 V	8 V	9 V
6	In tungsten filament, the current given to produce the beam of electrons by thermionic emission	0.1 A	0.2 A	CA A	0.4 A
7	One megabyte is equal to	1000 KB	1004 KB	1014 KB	✓1024 KB
8	The output of a two-input OR gate is 0 when	$\checkmark$ A = 0, B = 0	A = 1, B	A = 0, B = 1	A = 1, B =
9	In CRO potential of grid is	Positive	✓ Negative	Zero	Neutral
10	The equation of AND operation is	$\checkmark$ X = A.B	X = A + B	$X = \overline{A}$	$X = \overline{A + B}$
11	The equation of OR operation is	X = A.B	$\checkmark X = A + B$	$X = \overline{A}$	$X = \overline{A + B}$
13	The equation of NOT operation is	$X = \overline{A} B$	X = A + B	$\checkmark X = \overline{A}$	$X = \overline{A + B}$
14	The equation of NAND operation is	$\checkmark X = AB$	X = A + B	$X = \overline{A}$	$X = \overline{A + B}$
15	The equation of OR operation is The equation of NOT operation is The equation of NAND operation is The equation of NOR operation is One byte is equal to Number of input terminals in NOT gate	X = A.B	X = A + B	$X = \overline{A}$	$\checkmark$ X = $\overline{A}$ + $\overline{A}$
16	One byte is equal to	0 bit	1 bit	4 bits	√8 bits
17	Number of input terminals in NOT gate	1√	2	3	4

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